

5-13-02
m

Attorney's Docket No. 5649-917

PATENT

J1002 U.S. Pro
10/04/2002
01/09/02



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Chung et al.

Serial No.: To Be Assigned

Filed: Concurrently Herewith

For: METHODS OF FORMING AN INTEGRATED CIRCUIT CAPACITOR IN WHICH
A METAL PREPROCESSED LAYER IS FORMED ON AN ELECTRODE
THEREOF

Date: January 9, 2002

BOX PATENT APPLICATION
Commissioner for Patents
Washington, DC 20231

INFORMATION DISCLOSURE STATEMENT

Sir:

Attached is a list of documents on form PTO-1449 together with a copy of each identified document. It is requested that these documents be considered by the Examiner and officially made of record in accordance with the provisions of 37 C.F.R. § 1.97 and Section 609 of the MPEP. The Commissioner is hereby authorized to charge any additional fee, which may be required, or credit any refund, to our Deposit Account No. 50-0220.

Respectfully submitted,



D. Scott Moore

Registration No. 29,011



20792

PATENT TRADEMARK OFFICE

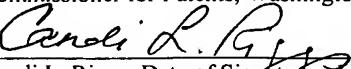
Tel (919) 854-1400
Fax (919) 854-1401

CERTIFICATE OF EXPRESS MAILING

"Express Mail" mailing label number EL 733099276 US

Date of Deposit: January 9, 2002

I hereby certify that this paper or fee is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed to BOX PATENT APPLICATION, Commissioner for Patents, Washington, DC 20231.


Candi L. Riggs; Date of Signature: January 9, 2002